

FIG 1

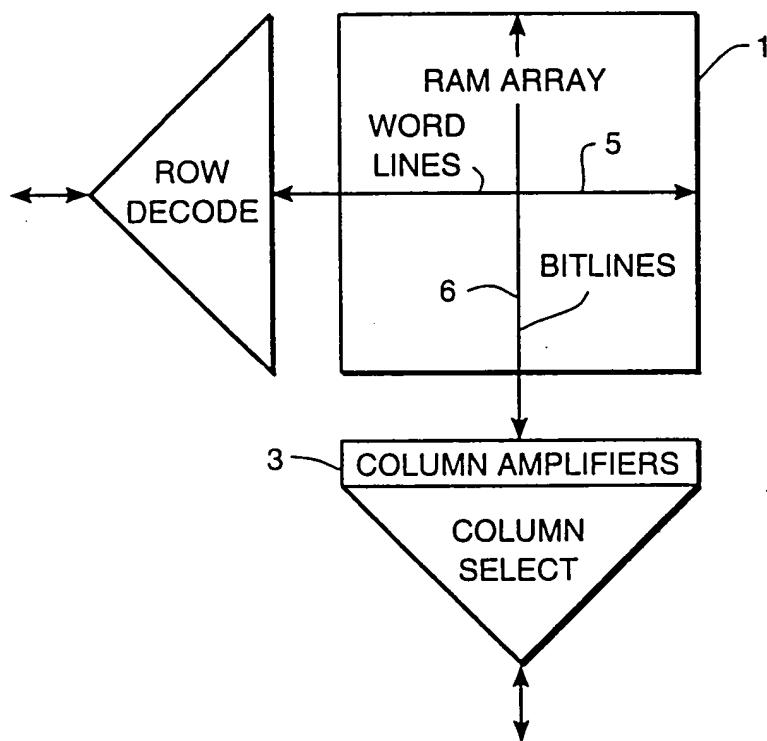


FIG - 2

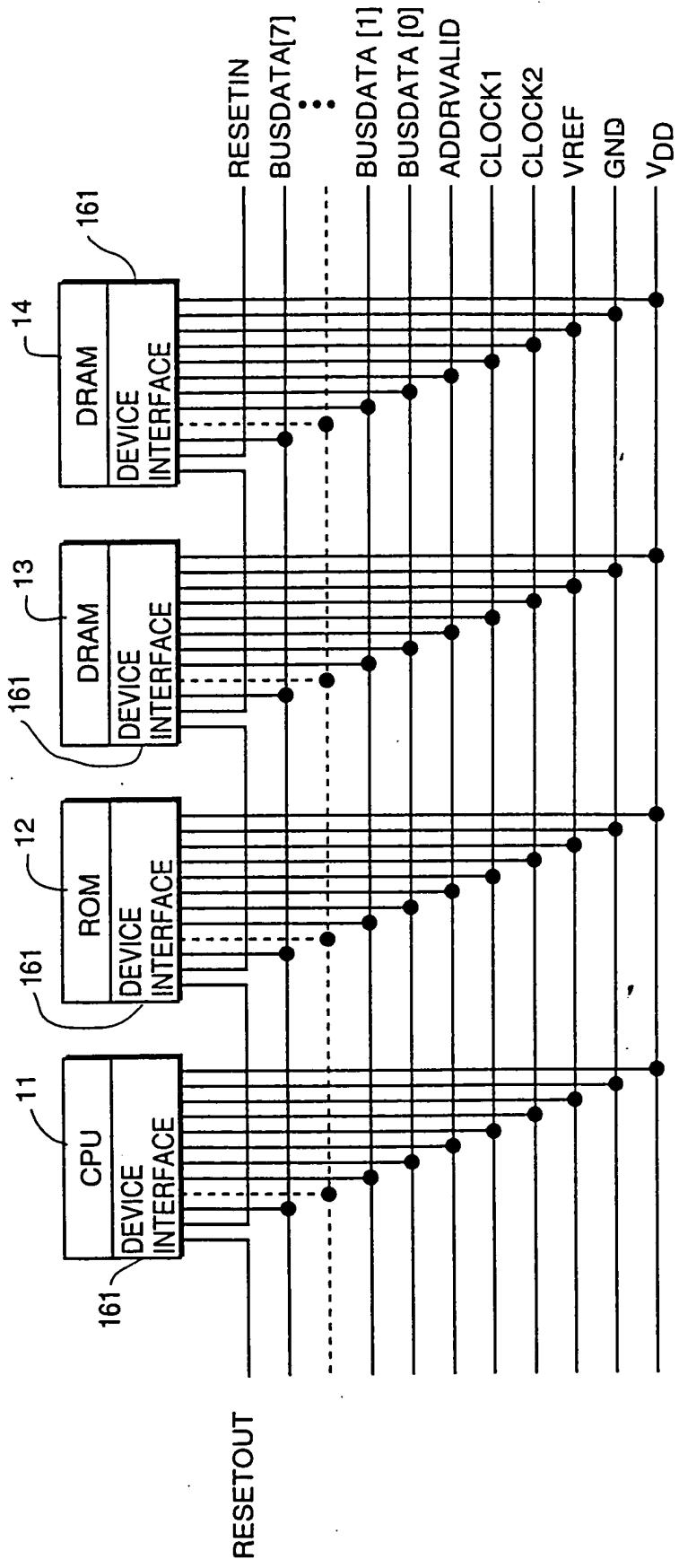
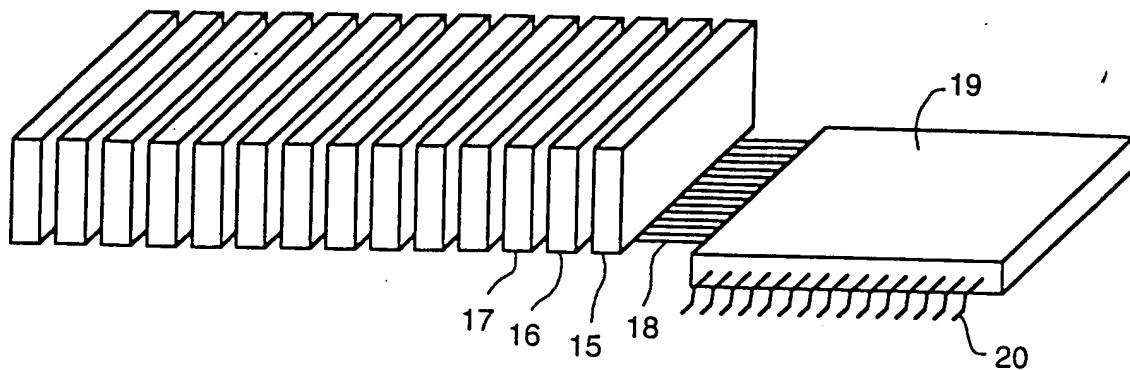


FIG 3



REGULAR ACCESS

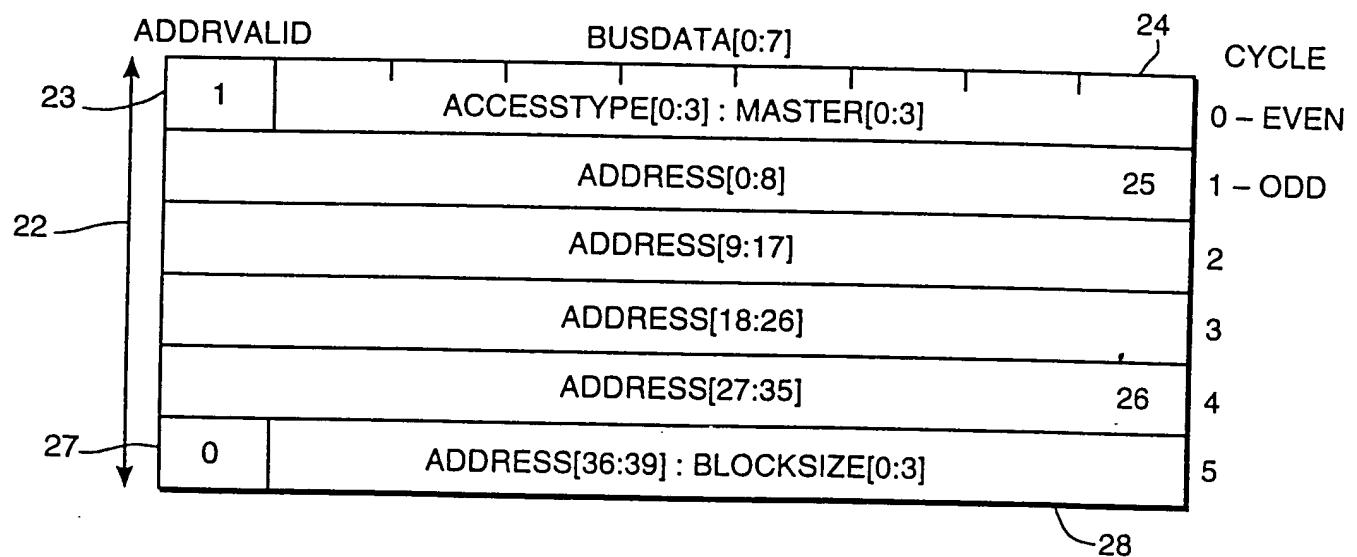


FIG 4

REJECT (NACK) CONTROL PACKET

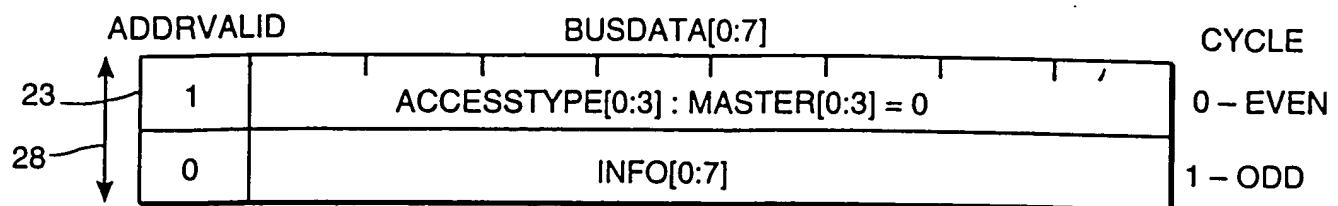


FIG _ 5

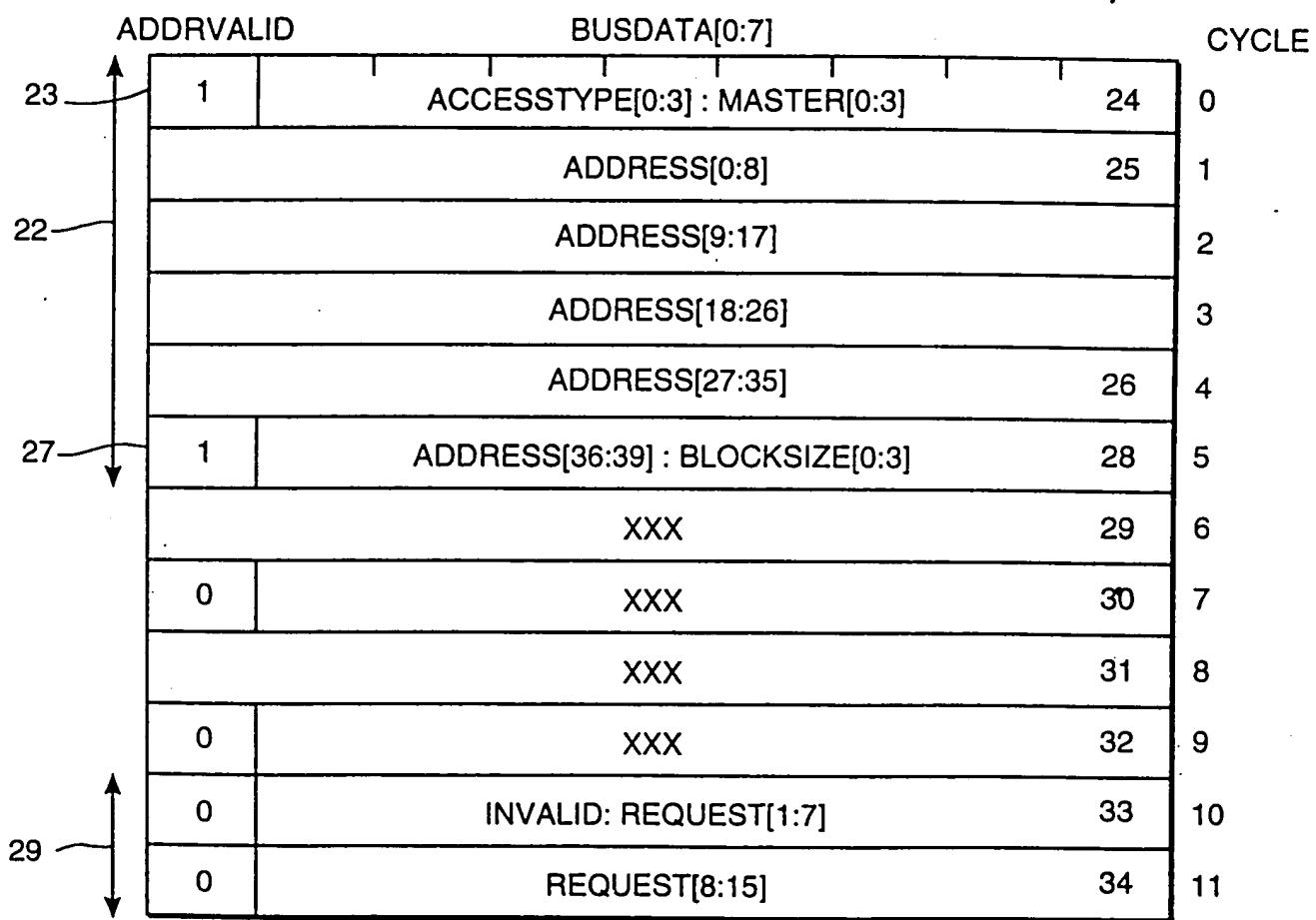
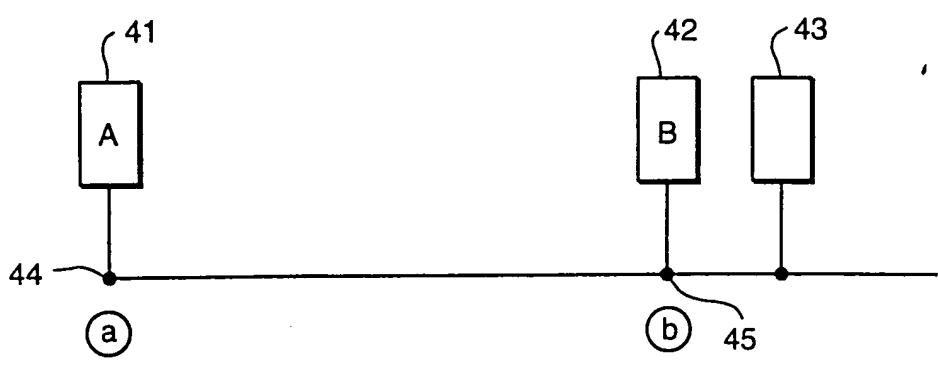


FIG _ 6



FIG_7A

open state, due to noise at
the input, the output voltage is
not 0 or 1, but some intermediate
value between 0 and 1.

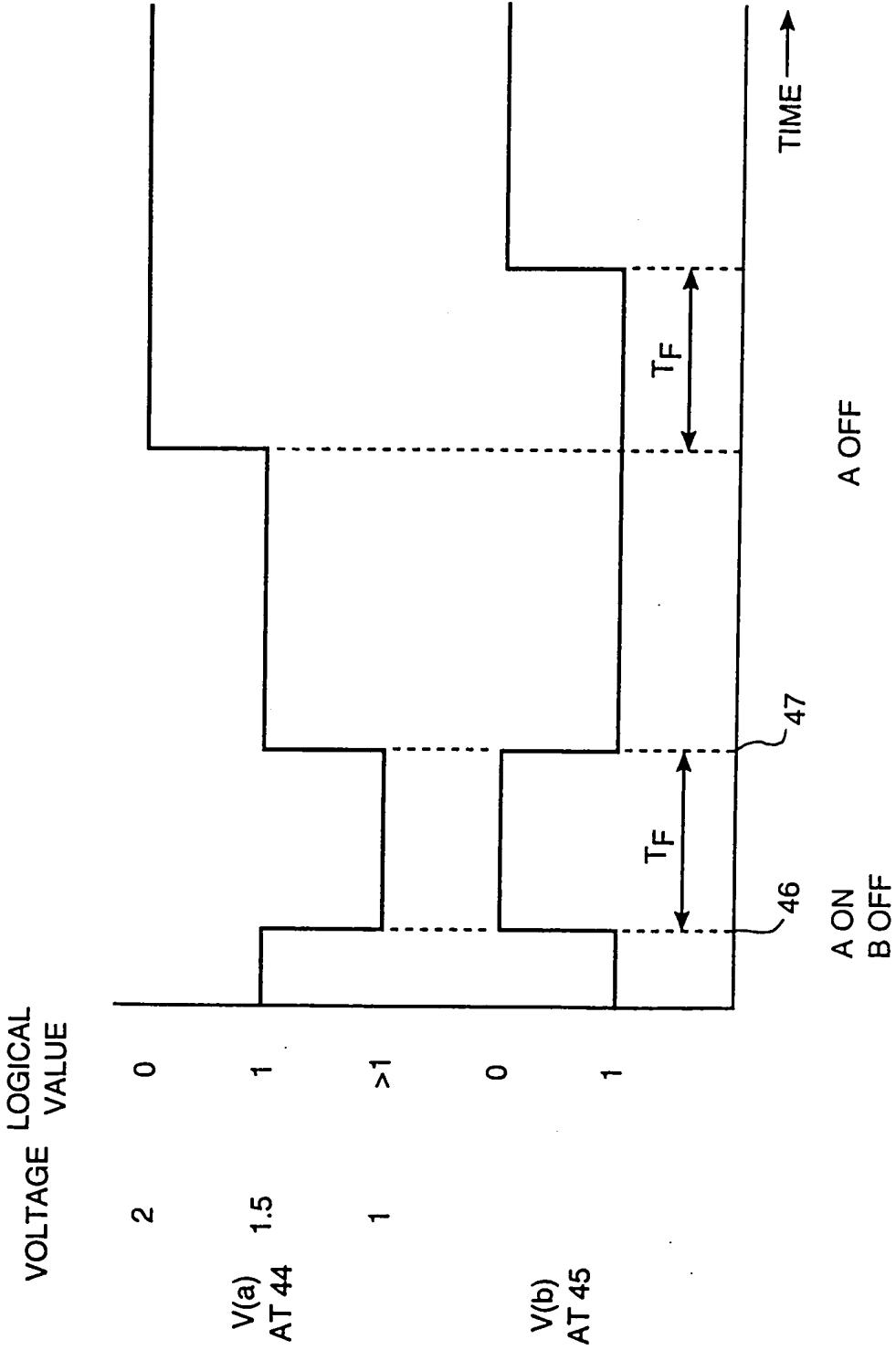
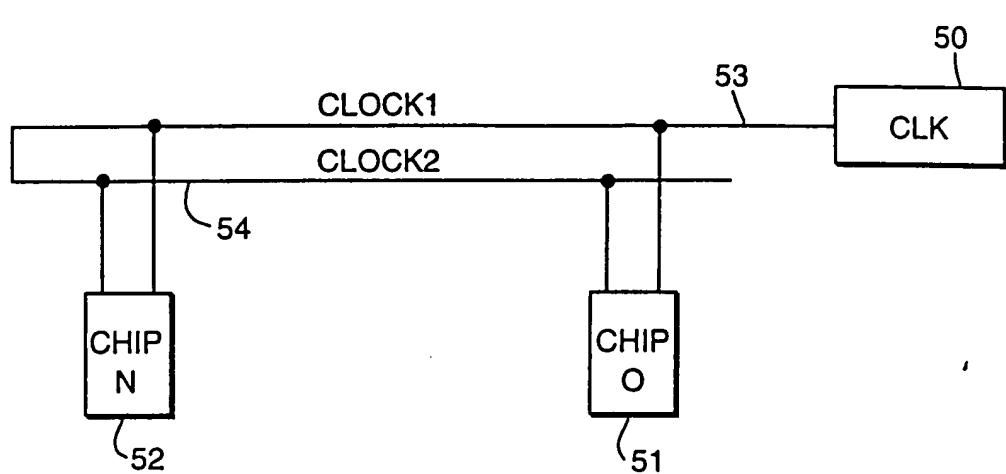
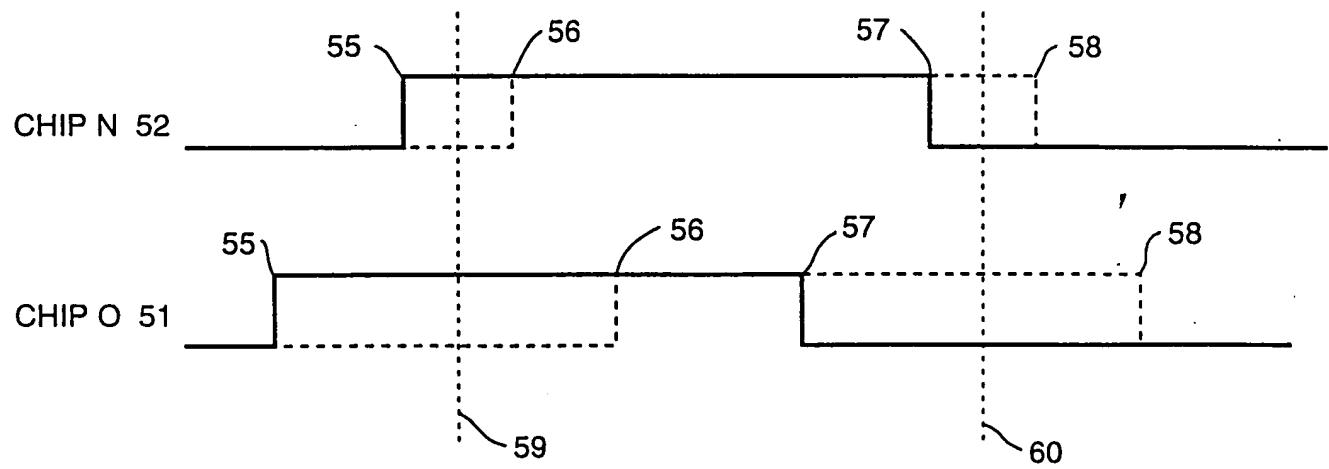


FIGURE 7B

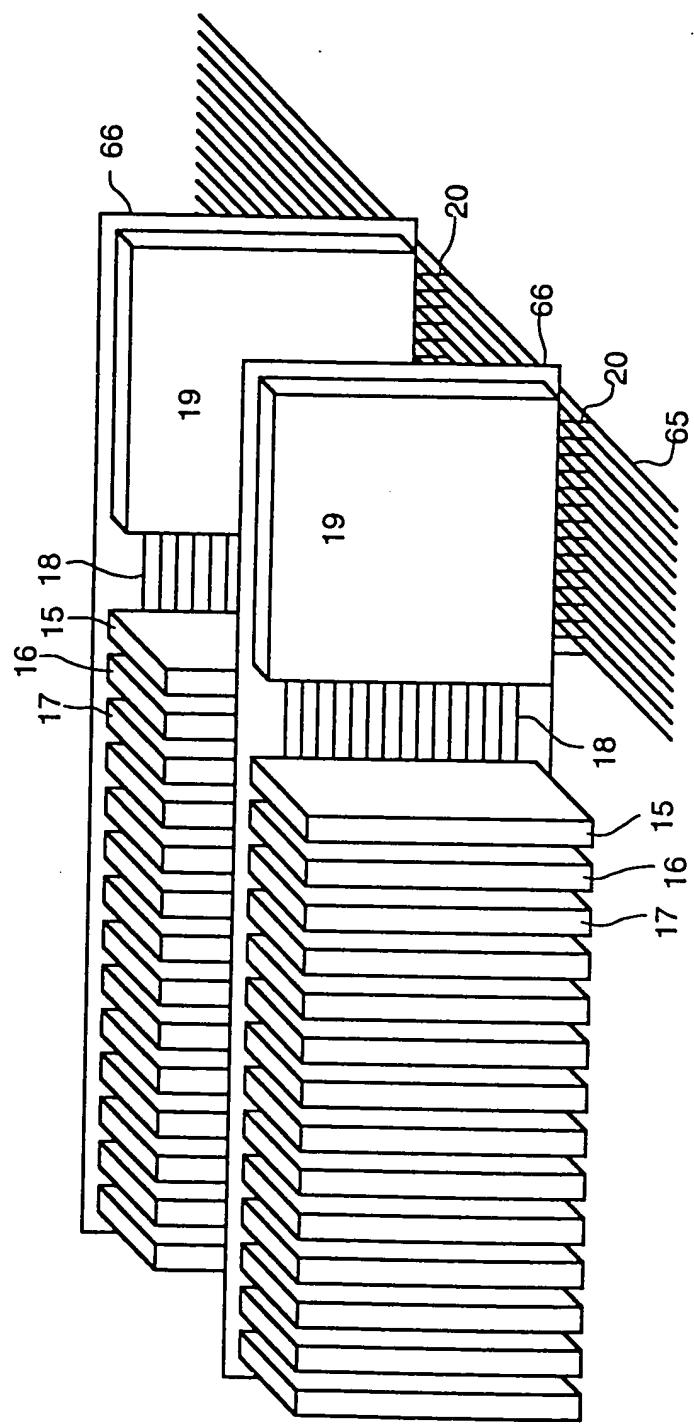


FIG_XA



FIG_XB

FIG. 5



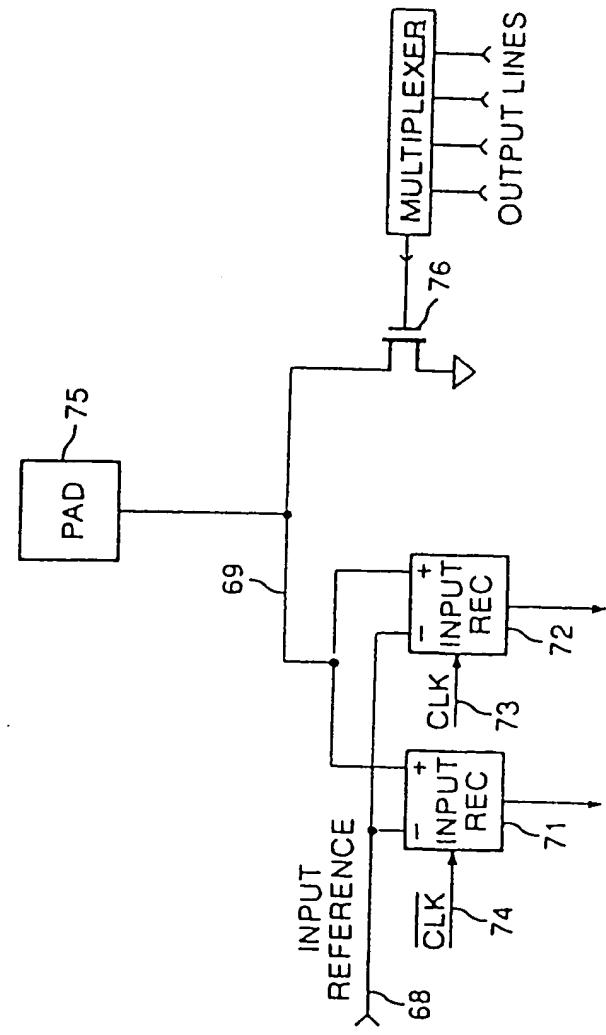


FIG. 10

FIG. II

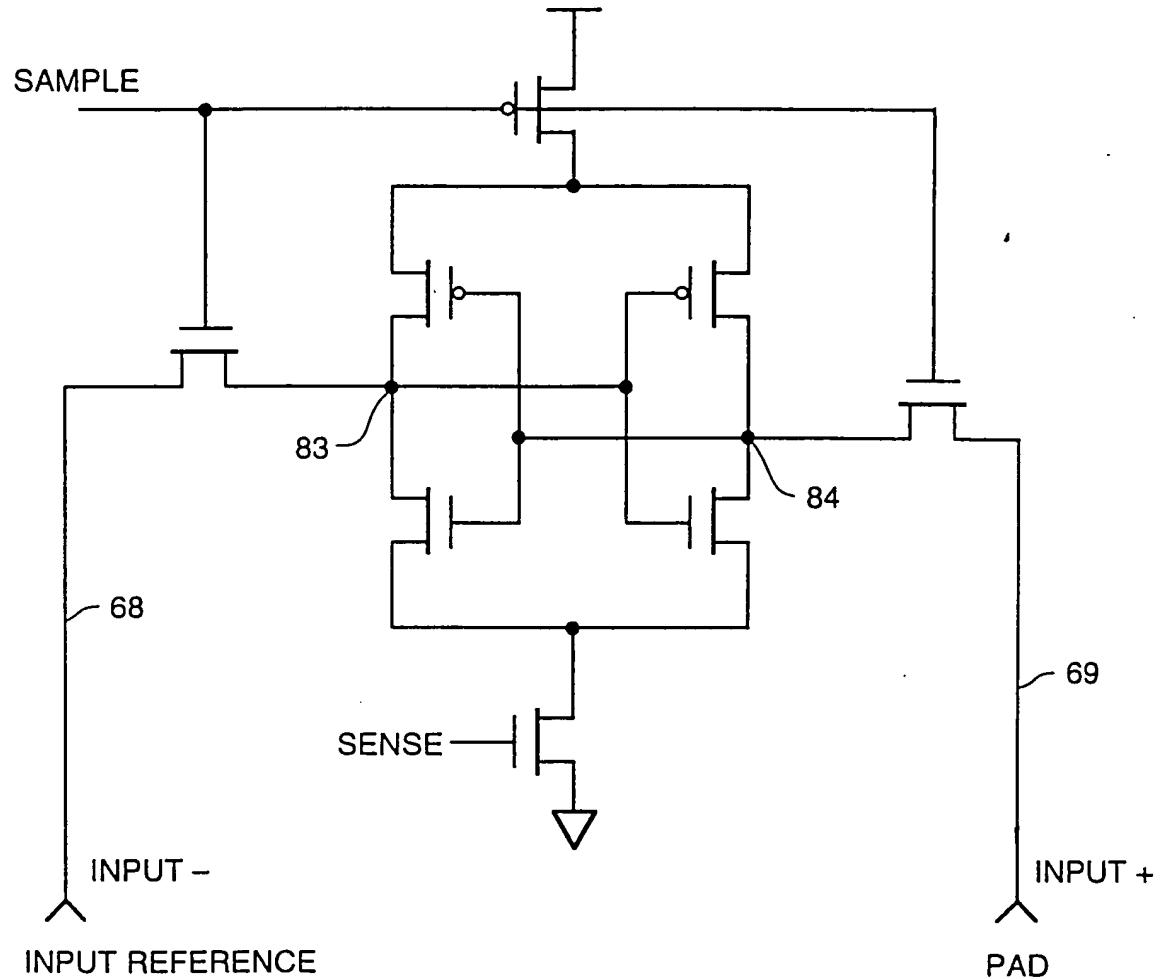
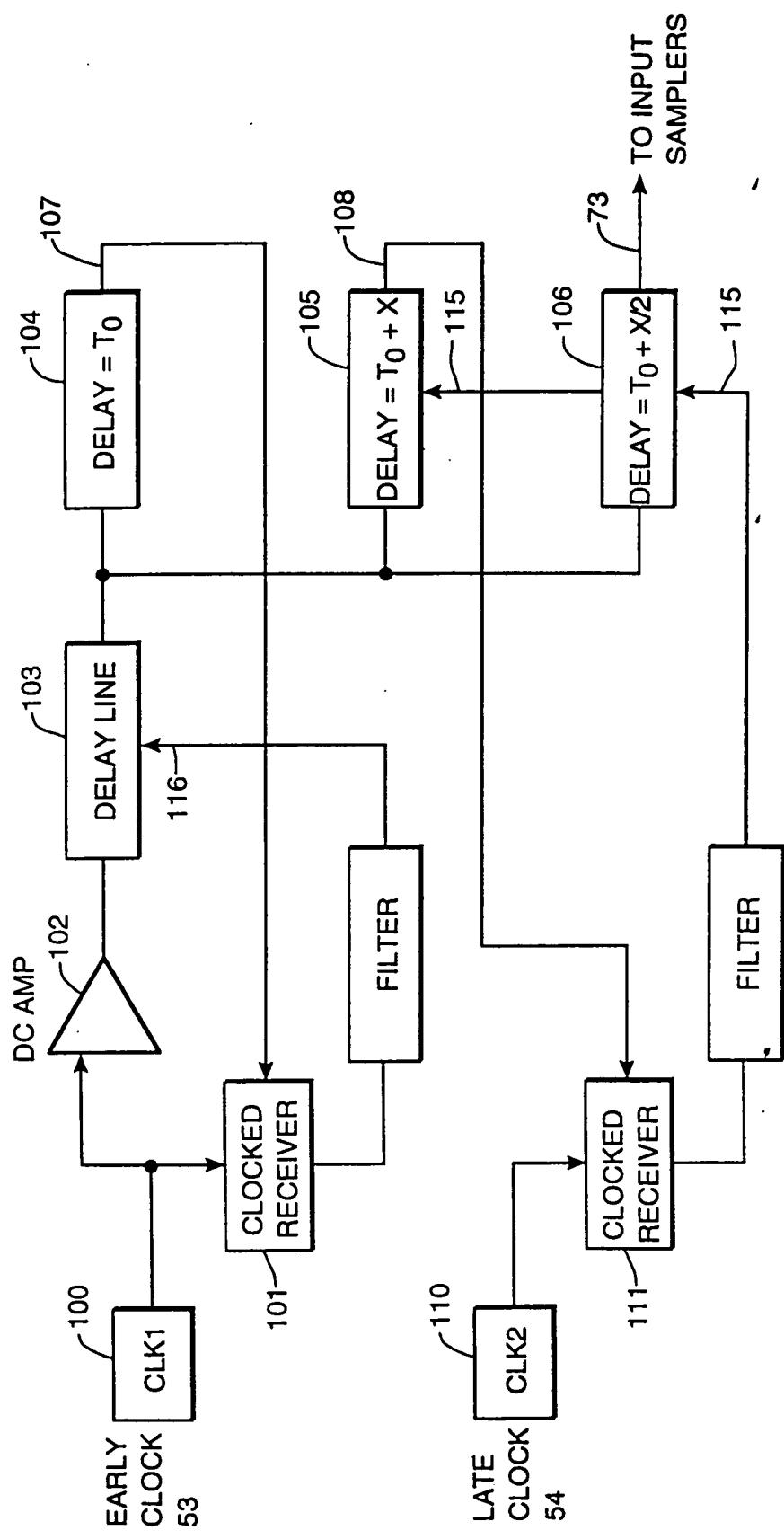
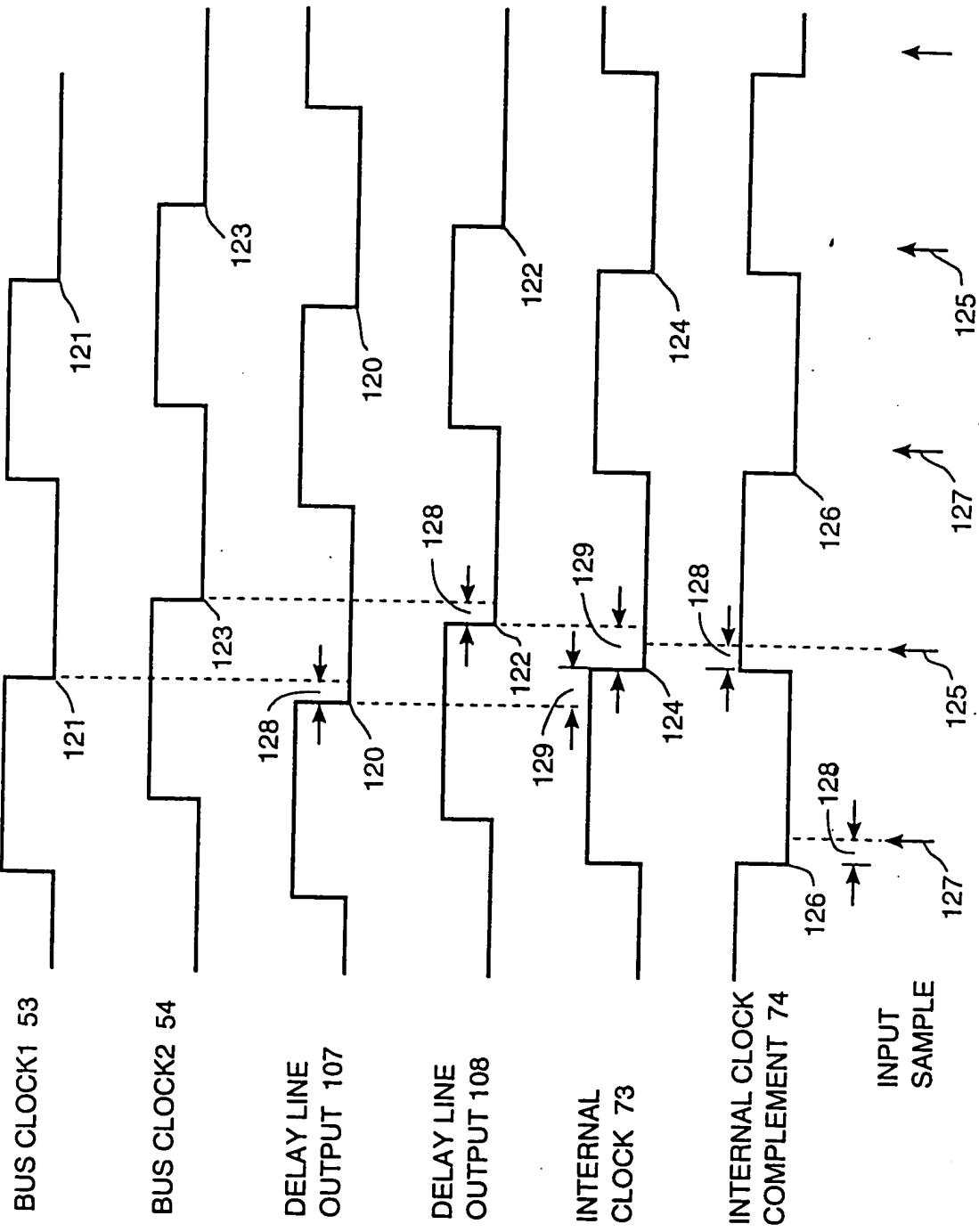


FIGURE 12

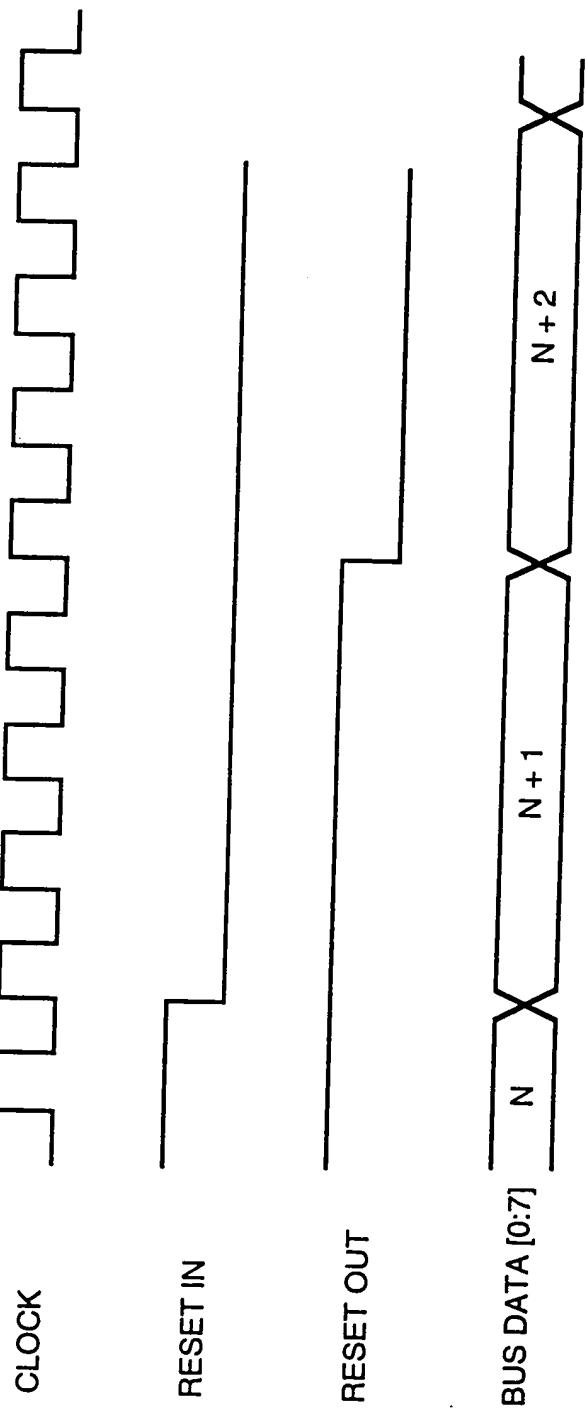


وَمِنْ أَنْتَ أَنْتَ رَبُّ الْأَنْوَارِ إِنَّكَ مَنْ فِي الْعُوْلَىٰ

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FILE_14

FIG_15

